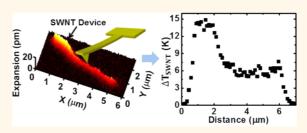


# Quantitative Thermal Imaging of Single-Walled Carbon Nanotube Devices by Scanning Joule Expansion Microscopy

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**ABSTRACT** Electrical generation of heat in single-walled carbon nanotubes (SWNTs) and subsequent thermal transport into the surroundings can critically affect the design, operation, and reliability of electronic and optoelectronic devices based on these materials. Here we investigate such heat generation and transport characteristics in perfectly aligned, horizontal arrays of SWNTs integrated into transistor structures. We present quantitative assessments of local thermometry at individual SWNTs in these arrays,



evaluated using scanning Joule expansion microscopy. Measurements at different applied voltages reveal electronic behaviors, including metallic and semiconducting responses, spatial variations in diameter or chirality, and localized defect sites. Analytical models, validated by measurements performed on different device structures at various conditions, enable accurate, quantitative extraction of temperature distributions at the level of individual SWNTs. Using current equipment, the spatial resolution and temperature precision are as good as  $\sim$ 100 nm and  $\sim$ 0.7 K, respectively.

**KEYWORDS:** single-walled carbon nanotube · scanning Joule expansion microscopy · thermal expansion · heat generation · temperature distribution · resolution · chirality change · defect

uperior electrical and thermal properties of single-walled carbon nanotubes (SWNTs) enable high-performance transistors,<sup>1,2</sup> advanced interconnects,<sup>3</sup> and other components of relevance to various existing and emerging forms of electronics. Heat generation and transport characteristics of devices that incorporate SWNTs are critical in understanding the fundamental properties and engineering considerations in device design. Such issues are particularly important because all known growth techniques yield SWNTs with distributions in diameters and chiralities and with densities of defects that can affect device performance<sup>4,5</sup> either directly or indirectly through non-uniform distributions of Joule heating.<sup>6,7</sup> Although optical techniques, such as Raman<sup>8-10</sup> and infrared spectroscopy,<sup>11,12</sup> can map the

distributions of temperature in SWNT devices, their limits in spatial and temperature resolution are defined by wavelengths of visible and infrared light and by background noise in the sensors, respectively. Methods based on atomic force microscopy (AFM), on the other hand, have the potential to overcome such limitations, thereby enabling studies of thermal transport at significantly improved resolution. One such technique, known as scanning thermal microscopy (SThM), offers useful capabilities in characterizing temperature distributions in nanometer-scale electronic devices.13-17 In SThM, a specialized tip or cantilever incorporates an integrated temperature sensor that can be scanned over a surface to measure temperature with submicrometer spatial resolution.<sup>17–21</sup> Key challenges for SThM

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are in integration and calibration of the required, specialized thermal probes. In particular, heat flow between the cantilever and substrate can be difficult to describe accurately, thereby diminishing the quantitative capabilities of the measurements.<sup>22,23</sup> Strategies for reducing these artifacts include extensive modeling, methods to subtract the background signals, and use of vacuum conditions during measurement.<sup>22-24</sup> Scanning Joule expansion microscopy (SJEM)<sup>25,26</sup> offers an alternative that avoids these challenges in which conventional AFM cantilevers measure nanometer-scale thermal expansions as a means to reveal underlying distributions of temperature.

Here we report the application and further development of the SJEM method to investigate heat generation and transport in transistors that incorporate straight, horizontally aligned arrays of individual SWNTs. The small dimensions (diameters  $\sim$ 0.5–1.7 nm)<sup>27</sup> of these SWNT-based heat sources and their quasi-onedimensional nature differentiate them from structures that have been examined previously by SJEM.<sup>28-30</sup> The images directly and immediately reveal important physics associated with Joule heating in SWNT devices under various electrical bias conditions, in ways that can be used to identify defects and other nonideal features of the SWNTs, as well as variations in electrical switching behaviors. Quantitative interpretation of the results with analytical models and finite element analysis (FEA) for heat flow and thermal expansion enables accurate determination of the distributions in temperature. Studies using different device structures, examined at various operating frequencies, provide insights into the limits in spatial and temperature resolution and the important mechanics that govern interactions between the tip and sample. The results demonstrate that SJEM can be a useful tool for exploring thermal and electrical properties in SWNT devices and, by extension, other components that incorporate nanomaterials.

## **RESULTS AND DISCUSSION**

Figure 1 shows a schematic illustration of the setup for SJEM measurement on a device with several straight, well-separated, aligned SWNTs on a SiO<sub>2</sub>/Si substrate. An alternating voltage  $V(t) = V_{ds} \cos(2\pi f t)$ applied between the drain and source contacts causes Joule heating in the SWNTs and an associated timeoscillating rise in temperature (with frequency 2f). A direct current (DC) voltage applied to the silicon wafer serves as a back gate to control the electrostatics. A layer of poly(methyl methacrylate) (PMMA) spincast uniformly onto the device substrate protects the SWNTs during contact scanning and prevents electronic coupling between the tip and the device components. This film also amplifies the thermomechanical deformations (oscillating at 2f) induced by thermal expansion that follow heat flow into the surroundings from the SWNT.<sup>26</sup> An AFM probe

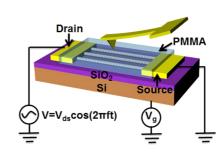


Figure 1. Schematic illustration of the experimental setup for scanning Joule expansion microscopy (SJEM) imaging of a SWNT device that incorporates several parallel, aligned tubes. The device consists of three terminals: two metal pads that contact the SWNTs to serve as the drain (alternating voltage) and source (grounded), and a third (doped silicon substrate) that serves as a gate. A layer of poly(methyl methacrylate) (PMMA) covers the device to protect the SWNTs and amplify the thermo-mechanical expansion caused by the Joule heating. An AFM probe operating in contact mode detects this expansion.

operating in contact mode and scanning the top surface of the PMMA measures the vertical displacements. Detection with a lock-in amplifier set to the frequency 2f reveals deflections of the AFM cantilever due to electrically driven thermal expansion. (Details of the SJEM setup appear in the Supporting Information, part 1 and Figure S1.) The two-dimensional (2D) spatial maps of thermo-mechanical expansion (i.e., amplitude of the oscillating vertical displacement) that emerge from measurements of this sort are directly related to the heat generation in the SWNTs and thermal diffusion and, as a result, the associated temperature distributions.<sup>25</sup> Figure 1 corresponds to the case of SWNTs incorporated into a field effect transistor (FET) in a back-gated geometry, where the gate bias is  $V_{q}$ .

The arrays contain a mixture of metallic and semiconducting SWNTs. Figure 2a shows an AFM topographical map of a device with channel length  $L \sim 3.7 \,\mu m$ that incorporates two SWNTs spaced by  $\sim$ 1.5  $\mu$ m on a SiO<sub>2</sub>/Si (300 nm/300  $\mu$ m) substrate, with the drain and source contacts on the left and right, respectively. The device is coated with a layer of PMMA (thickness ~310 nm) for imaging. Figure 2b-f presents SJEM images collected at f = 25 kHz,  $V_{ds} = 3$  V and at gate voltages of  $V_{q} = 20, 10, 0, -10, and -20 V$ , respectively. The expansion signals coincide with the locations of the SWNTs, for which maxima and minima occur in the channel and near the metal contacts, respectively, as would be expected based on heat flow considerations and operation of the device in the long-channel, diffusive transport regime.<sup>6,15,31</sup> For the SWNT in the upper region of this image, the expansion is similar for all gate voltages. By contrast, the SWNT in the lower region shows expansion that is nearly undetectable at  $V_{\rm q} = 20$  V but which gradually increases as  $V_{\rm q}$  decreases and then becomes more negative. The expansion profiles along the upper and lower SWNTs appear in Figure 2g,h, respectively, for all  $V_{q}$ . The behaviors observed for the upper SWNT (Figure 2g) are consistent

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with a metallic type and with heat flow into the substrate along the length of the SWNT and into the metal electrodes near the contacts, similar to previous observations.<sup>7,14,15</sup> Features observed for the lower SWNT are consistent with semiconducting behavior and hole transport. The maximum expansion signal at

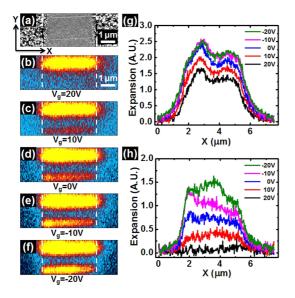


Figure 2. (a) Topographical AFM image of two SWNTs in the same FET. (b–f) SJEM images of the SWNTs at different gate voltages:  $V_g = (b) 20 V$ , (c) 10 V, (d) 0 V, (e) –10 V, and (f) –20 V. These images used  $V_{ds} = 3 V$ , f = 25 kHz, and PMMA thickness ~310 nm. The white dashed lines indicate the edges of the metal contacts. (g,h) Thermo-mechanical expansion profiles along the two SWNTs at different  $V_g$  extracted from (b–f). The magnitudes and shapes of the expansion profiles from the upper SWNT do not change substantially with  $V_g$  (g), indicating metallic behavior; those from the lower SWNT show substantial changes with  $V_g$  (h), consistent with p-doped semiconducting behavior.

 $V_{\rm g} = -20$  V is about 10 times larger than that at  $V_{\rm g} = 20$  V, the latter of which is comparable to the noise level in the measurement. More interestingly, the shapes of the profiles change somewhat with  $V_{\rm g}$ , suggesting variations of power dissipation along the SWNT during the switching process, presumably caused by some combination of electrostatics, charge transport in the SWNTs, and variations in local charge distributions, many aspects of which are influenced by time-dependent levels of hysteresis that are present in devices of this sort.<sup>32</sup>

In addition to basic information such as identification of metallic and semiconducting behaviors, SJEM can reveal other electrical properties associated with heat dissipation. For example, Figure 3a shows a representative transfer characteristic and a scanning electron microscope (SEM) image (inset) of a device incorporating a single metallic SWNT formed by direct growth on a SiO<sub>2</sub>/Si (200 nm/500  $\mu$ m) substrate (see Methods). Figure 3b presents an SJEM image for  $V_{ds}$  = 3 V,  $V_{q} = 0$  V, f = 30 kHz and with a PMMA coating thickness of  $\sim$ 120 nm. Two distinct segments with substantially different expansion signals appear along the length of the SWNT. A close examination (Figure 3c) of the device, without the PMMA, reveals that the direction of the SWNT changes abruptly along the channel at the point that coincides with the change in thermal behaviors. AFM measurements of diameters yield  $1.0(\pm 0.25)$  and  $1.2(\pm 0.25)$  nm for the hotter and colder segments, respectively. These observations suggest that a change in chirality and diameter, which can occur during the growth,<sup>33</sup> results in different electrical resistances and power dissipation characteristics. Application of  $V_{ds}$  without the PMMA coating leads to the

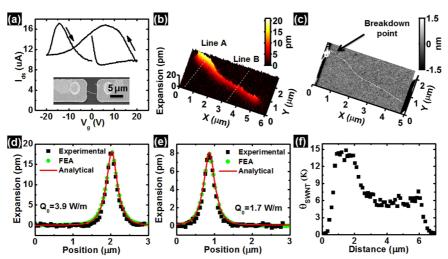


Figure 3. (a) Transfer curve of a single SWNT measured at  $V_{ds} = 3 V$  (DC voltage); the response indicates metallic behavior. The inset shows a scanning electron microscope (SEM) image of the device. (b) SJEM image of the same device, collected with  $V_{ds} = 3 V$ , f = 30 kHz, and PMMA thickness ~120 nm. (c) Topographical AFM image of the SWNT collected after removing the PMMA and operating the device at high bias in air. The location of degradation (breakdown point) coincides with that of the highest thermal expansion signal. (d,e) Expansion signal profiles (black solid square) along the cross sections marked by dotted lines A and B in (b), respectively. Green solid circles and red lines represent results from 2D FEA and analytical models, respectively, in which the power density,  $Q_0$ , serves as a fitting parameter:  $Q_0 = 3.9 \mu W/\mu$ m for line A and  $Q_0 = 1.7 \mu W/\mu$ m for line B. (f) Calculated temperature distribution along the SWNT based on the SJEM measurement.

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destruction of the SWNT to form a small gap (black arrow) that matches the location of the largest expansion signal, as might be expected for a thermally driven breakdown process.<sup>6,31</sup>

SJEM yields quantitative information about the power dissipation and temperature field that provides further insights into the physics of SWNT devices. Experimental demonstration of these capabilities is most clearly accomplished through SJEM measurements on devices that incorporate single, individual SWNTs, such as the one in Figure 3a. Figure 3d, e shows the expansion signal profiles (solid squares) for this device, extracted along cross sections marked by dotted lines A and B in Figure 3b, respectively. To quantitatively explain these profiles, we performed a 2D analytical study as well as a finite element analysis (FEA) of the coupled thermal and mechanical responses. Here a 2D treatment, for locations away from the contacts, represents a reasonable approximation due to the long lengths and small diameters (i.e., large aspect ratio) of the SWNTs (see Supporting Information part 5 for the validation of the 2D models). Moreover, the typical thermal transfer length along the SWNT near the contacts is a few hundred nanometers,<sup>6,31</sup> which is much smaller than the overall length of the SWNT.

The analytically modeled system in this case includes a PMMA coating with thickness  $h_0$  on a layer of SiO<sub>2</sub> with thickness  $h_1$ . The silicon substrate underneath is assumed to be infinite. The SWNT has radius  $r_0$  and lies at the interface between the PMMA and SiO<sub>2</sub>. The center of the SWNT marks the origin of a Cartesian coordinate system with x along the interface, in a direction perpendicular to the SWNT, and with y into the Si substrate (see Supporting Information, Figure S2). The temperature distribution is obtained by solving the heat diffusion equation

$$\frac{\partial\theta}{\partial t} - \alpha \left( \frac{\partial^2\theta}{\partial x^2} + \frac{\partial^2\theta}{\partial y^2} \right) = 0 \tag{1}$$

where  $\alpha$  is the thermal diffusivity and  $\theta = T - T_{\infty}$  is the increase in temperature relative to the ambient temperature  $T_{\infty}$ . In the following, subscripts 0, 1, and 2 specify the physical properties of the PMMA,  $SiO_2$ , and Si, respectively. The boundary conditions include zero heat flux at the top surface of the PMMA ( $-k_0(\partial\theta)$ /  $\partial y|_{y=-h_0} = 0$ , where k is the thermal conductivity) and ambient temperature at infinity ( $\theta|_{v=\infty} = 0$ ). The temperatures and heat fluxes across all interfaces are assumed to be continuous, except at the location of the SWNT, where the heat generation requires  $k_0(\partial\theta)$  $\partial y|_{y=0^-} - k_1(\partial \theta/\partial y)|_{y=0^+} = Q/\pi r_0$  for  $|x| \le \pi r_0/2$ , where Q is the power dissipation per unit length along the SWNT. For an applied voltage  $V = V_{ds} \cos(\omega t)$  as in the experiments,  $Q = Q_0(1 + \cos(2\omega t))$  has both DC (*i.e.*,  $Q_0$ ) and AC (*i.e.*,  $Q_0 \cos(2\omega t)$ ) components, where  $\omega = 2\pi f$ is the angular frequency and  $Q_0$  is the amplitude of the oscillating power density. The resulting temperature rise and the thermal expansion will have both steadystate terms and time oscillating terms. Since SJEM measures the time-oscillating component of the expansion, we consider only the AC power input  $Q = Q_0 \cos(2\omega t)$  and the corresponding oscillating temperature rise and expansion but ignore the steady-state contribution (the steady-state terms can be obtained by letting  $\omega = 0$  in the final solution). To solve the heat transfer equation, we represent the oscillating temperature in the PMMA as  $\theta(x,y,t) = \theta_0(x,y)\exp(2i\omega t)$  and calculate  $\theta_0(x,y)$  through the Fourier cosine transform (details appear in the Supporting Information, part 2) to find

$$\theta_{0}(x,y) = Q_{0}\sqrt{\frac{2}{\pi}} \int_{0}^{+\infty} A_{0}[\exp(y\sqrt{s^{2}+q_{0}^{2}}) + \exp(-y\sqrt{s^{2}+q_{0}^{2}} - 2h_{0}\sqrt{s^{2}+q_{0}^{2}})] \cdot \cos(sx) ds$$
(2)

where

$$A_{0} = (\kappa + 1)\exp(h_{0}\sqrt{s^{2} + q_{0}^{2}})\sin\left(\frac{\pi r_{0}}{2}s\right)$$

$$/\pi r_{0}s\sqrt{2\pi(s^{2} + q_{1}^{2})}k_{1}\left[(1 - \kappa)\cosh(h_{0}\sqrt{s^{2} + q_{0}^{2}})\right]$$

$$+ (\kappa + 1)\frac{k_{0}\sqrt{s^{2} + q_{0}^{2}}}{k_{1}\sqrt{s^{2} + q_{1}^{2}}}\sinh(h_{0}\sqrt{s^{2} + q_{0}^{2}})\right]$$

$$\kappa = 1 - \frac{k_{2}}{k_{1}}\frac{\frac{\sqrt{s^{2} + q_{0}^{2}}}{\sqrt{s^{2} + q_{1}^{2}}}}{1 + \frac{k_{2}}{k_{1}}\frac{\sqrt{s^{2} + q_{1}^{2}}}{\sqrt{s^{2} + q_{1}^{2}}}}\exp(-2h_{1}\sqrt{s^{2} + q_{1}^{2}}),$$

$$q_{0}^{2} = \frac{2i\omega}{\alpha_{0}}, \quad q_{1}^{2} = \frac{2i\omega}{\alpha_{1}}, \quad \text{and} \quad q_{2}^{2} = \frac{2i\omega}{\alpha_{2}}$$

As an approximation, the contribution of SiO<sub>2</sub> and Si to the thermal expansion can be neglected since their coefficients of thermal expansion (CTE) ( $\sim 1 \times 10^{-6}$ ) are more than 10 times smaller than that of PMMA ( $\sim$ 5  $\times$  $10^{-5}$ ). (We note that, although the Si substrate is thick, the temperature rise is significant only near its surface and diminishes quickly. This behavior leads to an approximately spatially uniform contribution to the total expansion, as shown in Figure S6.) Neglecting expansion in SiO<sub>2</sub> and Si allows an analytical study of the SJEM signal by considering expansion in a layer of PMMA that is traction-free at its top surface ( $y = -h_0$ ) and fully constrained at its base (y = 0). The full solution, which appears in the Supporting Information, part 2, indicates that at low frequencies, when the thermal diffusion length for the PMMA is much larger than the PMMA thickness (for purposes here, we define this length as  $L_{\rm D} = (\alpha_0/\omega)^{1/2} \gg h_0$ ), the temperature throughout the thickness of PMMA is approximately equal to its surface temperature, and the amplitude of the oscillating vertical displacement at the PMMA top

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agnanc www.acsnano.org surface (*i.e.*, the detected thermal expansion in experiment) can be written simply as

$$|u_{y}(x, -h_{0})| = \frac{1+\nu_{0}}{1-\nu_{0}} \beta_{0}h_{0}|\theta_{0}(x, -h_{0})| \qquad (3)$$

where  $\nu_0$  and  $\beta_0$  are the Poisson's ratio and CTE of PMMA, respectively (see Figure S4 for comparisons of the simplified formula with the full solution at different frequencies). We also performed coupled thermomechanical FEA to verify the results of the analytical treatment (see Methods section).

On the basis of such models, the measured crosssectional SJEM profiles can be fit using the power dissipation per unit length  $Q_0$  as the only free parameter, with all other material constants taken from the literature<sup>34–43</sup> (see Supporting Information, Table S1). The results appear as red lines (analytical) and green dots (FEA) in Figure 3d,e. When  $Q_0 \sim 3.9 \ \mu W/\mu m$  for cross section A and  $\sim$ 1.7  $\mu$ W/ $\mu$ m for cross section B, the computed results match the measured ones remarkably well. Repetitively fitting each measured cross section yields  $Q_0$  as a function of position along the SWNT. Integration yields a total power of  $\sim$ 14.6  $\mu$ W, which compares well to the measured total input power of  $\sim$ 18  $\mu$ W. The fact that most of the power ( $\sim$ 81%) is dissipated in the device channel is consistent with modest resistances at the electrical contacts.

A simple, complementary way to consider the results is to note that the temperature increase of the SWNT can be written as  $\theta_{SWNT} = Q_0(1/g_{int} + 1/g_{sur})$ , where  $g_{int}$ is the interfacial thermal conductance per unit length along the tube<sup>6,31</sup> (at the interfaces between SWNT and PMMA, SiO<sub>2</sub>), and  $g_{sur}$  is the spreading thermal conductance from the surroundings of the SWNT to the ambient, including contributions from PMMA, SiO<sub>2</sub>, and Si. The analytical expressions in eq 2 yield values for  $g_{sur} = Q_0 / |\theta_0(0,0)| \approx 0.75$  and 0.73 W/m/K for  $r_0 = 0.5$ and 0.6 nm, respectively. Literature reports suggest that  $g_{\text{int}} = 2\pi r_0 h (g_{\text{int}} \approx 0.47 \text{ W/m/K for } r_0 = 0.5 \text{ nm and}$ 0.57 W/m/K for  $r_0 = 0.6$  nm), where  $h = 1.5 \times 10^8$  W/m<sup>2</sup>/K is the thermal coupling per unit area for typical metallic SWNTs.<sup>31</sup> With these parameters and radii of  $r_0 = 0.5$ and 0.6 nm for the hotter and colder segments, respectively, we obtain temperature distributions that are consistent with measurement and modeling (Figure 3f). This profile accurately reflects the distinct heating from the two segments, except within a few hundred nanometers (the thermal transfer length<sup>6,7</sup>) near the metal contacts and the joint region between the hotter and colder segments, where the temperature is somewhat overestimated (see Figure S8d and Figure S9) due to inaccuracies that arise from application of the 2D analysis to regions with non-uniform heating.

The achievable levels of spatial and temperature resolution are important characteristics of the measurement. To examine the limits of the technique, and to further understand heat flow from the SWNT, we

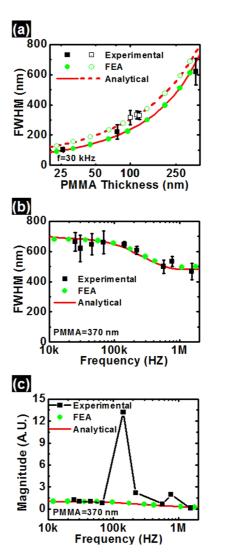


Figure 4. (a) Effects of PMMA and SiO<sub>2</sub> thicknesses on the resolution (represented by the full width at half-maximum, fwhm) in SJEM measurements: measurements, FEA, and analytical modeling of the widths (fwhm) for the expansion profiles across the SWNT, at f = 30 kHz. Data collected for PMMA of various thicknesses on two types of device substrates (hollow squares, hollow circles, and dashed lines correspond to SiO<sub>2</sub>/Si (200 nm/500  $\mu$ m); solid squares, solid circles, and solid lines correspond to SiO<sub>2</sub>/Si (90 nm/ 300  $\mu$ m)). The x-axis uses a log-scale. (b) Effects of frequency on the resolution of SJEM: measurements, FEA, and analytical modeling of the fwhm at different frequencies, for the case of PMMA and SiO<sub>2</sub> thicknesses of 370 and 90 nm, respectively. (c) Measurements, FEA, and analytical modeling of the expansion magnitude at different frequencies for the case of PMMA and SiO<sub>2</sub> thicknesses of 370 and 90 nm, respectively. Here, the thermo-mechanical simulation does not include the effects of tip-contact resonances that appear prominently around 150 kHz (see text and Supporting Information).

studied devices with different thicknesses of SiO<sub>2</sub> and PMMA. Figure 4a shows the characteristic widths (full width at half-maximum, fwhm) of the measured temperature signal along the sample surface and across individual SWNTs, similar to those in Figure 3d,e, on 90 nm (solid square) and 200 nm (hollow square) thick layers of SiO<sub>2</sub>, imaged at f = 30 kHz. Also shown are



results of FEA (solid and hollow circles) and analytical modeling (solid and dashed lines). Throughout the range of PMMA thicknesses (25 to 370 nm), the fwhm values of profiles for the 90 nm SiO<sub>2</sub> case are smaller than those for 200 nm SiO<sub>2</sub>. Likewise, the fwhm decreases almost linearly with thickness of the PMMA, corresponding to an increase in spatial resolution. (Results for PMMA thickness extending to 1.5  $\mu$ m appear in Figure S7a.) In all cases, experiment agrees well with modeling. We note that the temperature signal is a convolution of the thermo-mechanical expansion, which is mostly in the vertical direction, and the temperature field near the CNT, which varies in both the vertical and lateral directions.

The temperature resolution, defined as the temperature rise in the SWNT that causes the smallest detectable expansion signal, can be represented as  $\delta T$  =  $((\delta h)/(|u_y(0,-h_0)|/Q_0) (1/g_{int} + 1/g_{sur})$ , where  $\delta h$  is the noise equivalent height. This latter parameter originates from cantilever noise<sup>25</sup> and both mechanical and electrical noise in the AFM system. The thermomechanical expansion is almost entirely in the vertical direction, which simplifies analysis of the temperature resolution.<sup>28</sup> For typical experimental conditions (f <90 kHz, Asylum ACT 240 cantilever, bandwidth for the lock-in amplifier between 3.4 and 35 Hz),  $\delta h$  is between 0.7 and 1.6 pm. The temperature resolution  $\delta T$  also depends on the thermo-mechanical expansion of the PMMA (*i.e.*,  $u_v(0, -h_0)/Q_0$ ). As the PMMA thickness increases, the total expansion signal increases for a given temperature rise, thereby providing an improved temperature resolution (see Figure S7b for the expansion signal scaling with PMMA thickness). Finally, the interfacial thermal resistance between the SWNT and surroundings further decreases  $\delta T$ . The temperature resolution for the SWNT shown in Figure 3 is estimated to be  $\sim$ 0.7 K.

The dependence of the spatial resolution on operating frequency is also important and can be understood in terms of the thermal diffusion length. Figure 4b shows the fwhm values of the cross sections from the measured signals, for a SWNT on SiO<sub>2</sub>/Si (90 nm/ 300  $\mu$ m) with PMMA (370 nm) at different frequencies, including both experiment and modeling results. At low frequencies, the width remains nearly constant since the thermal diffusion length  $L_D$  of PMMA is large compared to its thickness. As the frequency increases, the width decreases, consistent with a reduction in  $L_{\rm D}$ . Further increases in frequency leads to another regime of frequency-independent behavior, as  $L_D$  becomes comparable to and less than the thickness of PMMA. The magnitudes of the expansion signals show frequency dependence as well as illustrated in Figure 4c (solid square). Here, the green solid circles and red solid line correspond to the FEA results and analytical simulations, respectively. The general trend is that the expansion magnitude decreases as the frequency

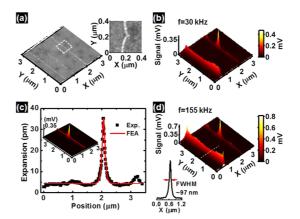


Figure 5. (a) Topographical AFM image of three SWNTs. The inset provides a magnified view of a "kink" in one of the SWNTs (highlighted by the white dashed box). (b) SJEM image of the same SWNTs. A strong peak appears at the position of the kink, consistent with significant heat generation at this location. (c) Expansion profile along the SWNT with the kink (solid square) and the results (red curve) based on a 3D FEA model that accounts for heat generation at the site of the defect. The power dissipation at the defect is calculated to be 2.9  $\mu$ W, while the rest of the SWNT has an average power density 3.0  $\mu$ W/ $\mu$ m. The inset shows the results of full 3D FEA simulations. (d) SJEM image collected at high frequency shows an enhancement in the signal. The overall image is similar to (a) except for the portion near the metal-SWNT contact where the local stiffness of the substrate and the step in topography may influence the tipcontact resonances. The lower left inset provides a crosssection profile along the dotted line in (c), showing a fwhm smaller than 100 nm.

increases, with the exception of several data points that show anomalously large signals at frequencies near 150 kHz (see Figure S10 for comparing images taken at f = 30 and 142 kHz). This behavior is related to resonant vibrations of the tip (see Supporting Information, part 6) and is not included in the thermo-mechanical simulation. The implication here is that quantitative SJEM measurements can be most easily made at low frequencies, where the AFM tip simply follows the thermal expansion profiles. Although high-frequency operation increases the spatial resolution, measurements in this regime can be difficult to analyze quantitatively due to complexities associated with tip-sample interactions.

As a demonstration of the versatility of the technique when implemented with PMMA thicknesses optimized according to the results described above, SJEM can be used to examine SWNTs that have defects. SWNT devices, even formed with pristine SWNT grown by chemical vapor deposition (CVD), often include defects<sup>44</sup> that can affect their electronic properties.<sup>5</sup> SJEM can image these defects to determine not only their spatial positions but also their effects on charge transport, via influence on local Joule heating. Figure 5a presents an AFM topographic image of a device with three SWNTs. One of the SWNTs has a prominent kink in the middle of the channel, as shown by the magnified image (inset in Figure 5a). This kink results from growth and can be considered as

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a topographic defect. Figure 5b shows a SJEM image of this device, imaged using a 25 nm thick coating of PMMA at a frequency of 30 kHz and  $V_g = 0$  V. Here, one SWNT exhibits nearly uniform expansion along its length; another SWNT shows almost no signal (due to high combined resistances in the contacts and this particular SWNT); the third exhibits a sharp peak corresponding to the site of the kink defect. Extraction of the expansion magnitude along this SWNT (black squares in Figure 5c) shows that the signal of the defect is about 8 times higher than the average level along the remainder of this SWNT, indicating substantial local heating.

Using a 3D FEA model that incorporates a point heating source (total power  $Q_{def}$ ) and a uniformly distributed line heating source (power density  $Q_{ave}$ ) to represent the defect and other parts of the SWNT, respectively (see Methods and Supporting Information, part 7), can match the experimental expansion profiles. Here, the interfacial thermal resistance  $g_{int}$ is adjusted to control the thermal diffusion along the SWNT (thermal transfer length  $L_{\rm T} \sim (kA(1/g_{\rm int} +$  $1/q_{sur}$ ))<sup>1/2</sup>, where k is the thermal conductivity and A is the cross-section area of the SWNT),<sup>6,7,31</sup> thereby the shape of the expansion profile. The power is selected to match the magnitude of the expansion. By using  $L_{\rm T} \sim$  40 nm,  $Q_{\rm def} \sim$  2.9  $\mu$ W, and  $Q_{\rm ave}$  = 3.0  $\mu$ W/ $\mu$ m, we achieved good agreement between the simulation and experiment, as shown by the red curve in Figure 5c. The plot in the inset shows the full solution from FEA (here, the simulated height values has been changed to the equivalent electronic readout values for better comparison with Figure 5a). The simulated  $L_{\rm H}$  $\sim$  40 nm is smaller than a previously reported value<sup>6</sup> (~200 nm) inferred from electrical breakdown of SWNTs on SiO<sub>2</sub>/Si. This difference is likely due in part to the additional, conformal PMMA layer, which facilitates heat removal from the SWNT (increases  $q_{int}$ ), and in part to a likely lower thermal conductivity in this SWNT near the defect location. We note that, although the power dissipated at the defect site only consists  $\sim$ 22% of the total input power, its temperature is

### **METHODS**

Fabrication and Characterization of SWNT Devices. Fabrication of the devices begins with the growth of aligned SWNTs on stable temperature (ST) cut single-crystal quartz (Hoffman Materials), using electron beam evaporated iron (0.1-0.4 Å) as the catalyst and ethanol as the feeding gas during the chemical vapor deposition (CVD) process.<sup>45,46</sup> Transfer of the SWNTs to a layer of thermally grown SiO<sub>2</sub> on a doped silicon wafer (SQI, Inc.) is carried out with methods described elsewhere.<sup>47</sup> Photolithography (AZ 5214), electron beam evaporation (AJA), and lift-off define the source–drain contacts (Ti/Pd, 1 nm/40 nm). Oxygen plasma etching of SWNTs that lie outside of the active channel through a photolithographically defined pattern of resist yields devices with one or several SWNTs. Some of the devices with single, individual SWNTs used tubes directly grown by CVD on estimated to be considerably higher ( $\sim$ 13 times) than that of the other regions of the SWNT.

For these results, the thin coating of PMMA enables high spatial resolution; low-frequency operation facilitates extraction of the power density and temperature quantitatively. To further increase the resolution as well as the signal magnitude, high-frequency imaging can be used, as shown in Figure 5d, collected at a frequency of 155 kHz. Here, the signal is enhanced due to resonant effects, as described in Figure 4c. The shape of the expansion is identical to the case evaluated at low frequency, except for regions of the sample near the edges of the metal contacts where contact stiffness of the AFM tip changes due, at least in part, to the sharp topographical features. This creates artificial peaks that do not accurately reflect the temperature (i.e., heat conduction through the metal contacts in these regions should reduce the temperatures locally).<sup>6,7</sup> The resolution of the image, as shown in the inset of Figure 5d (corresponding to cross sections marked by the dotted line), is better than 100 nm.

# CONCLUSIONS

The results provided here use SJEM to examine, in a quantitative fashion, the physics of heat generation and dissipation in a variety of devices that incorporate individual, isolated SWNTs as the active materials. Physical properties such as the chirality, diameter, and point defects strongly affect the measured temperature distributions. Analytical and FEA models account for all aspects of the thermo-mechanical response and provide tools for inferring temperature distributions from measurements of thermal expansion. By comparison to other techniques (Raman, IR, or SThM), SJEM has advantages of straightforward operation, guantitative interpretation, and high spatial resolution and sensitivity. The results provide an experimental and theoretical framework for accurate, precise thermal distribution measurements by SJEM, and the specific observations in SWNT devices yield insights into basic operational characteristics and heat flow properties in these systems.

SiO<sub>2</sub>/Si substrates with catalyst islands of ferritin (Sigma Aldrich) patterned by photolithography.<sup>48</sup> Scanning electron microscopy (SEM, Hitachi 4800) enables selection of those devices with a desired number of SWNTs. Use of a semiconductor parameter analyzer (Agilent 4156) reveals the electrical characteristics.

SJEM Imaging. PMMA (950 A4 or 950 A2; Micro Chem) spincast onto devices at different spinning speeds and durations yields films with desired thicknesses. PMMA diluted in methoxybenzene enables access to thicknesses smaller than 50 nm. Baking the devices at 110 °C for 5–10 min after coating removes the residual solvent. AFM (Asylum MFP 3D) measurements at step edges corresponding to locations of physical removal of the PMMA near the devices yield accurate values of the thicknesses. Wire bonding the device to a sample holder (Spectrum Semiconductor Materials) allows measurement



while providing electrical connections for applying electrical bias. All of the SJEM measurements are performed under ambient conditions, in air. Evaluating the force/displacement curve of the cantilever (Asylum Olympus AC240TS) before scanning defines the relationship between the deflection and detector signal. During SJEM imaging, a function generator (Agilent 33250A) provides a sinusoidal voltage to the source drain and a reference to the lock-in amplifier (Stanford SR844). A source meter (Keithley 2612) controls the back-gate voltage. The deflection signal induced by thermal expansion feeds into the lock-in amplifier to capture the amplitude and phase of the signal at twice the frequency of the oscillating applied potential (Figure S1).

Finite Element Analysis (FEA). FEA simulations rely on commercial software (COMSOL) with combined modules of heat conduction (transient) and mechanical stress-strain analysis (quasi-static). For the 2D model, a SWNT placed at the interface between the PMMA and SiO<sub>2</sub>, with power dissipation per unit length  $Q = Q_0(1 + \cos(2\omega t))$ , serves as the Joule heating source. The width of the simulated domain is 200  $\mu$ m. The thickness of the Si is 100  $\mu$ m (Figure S5). Performing the transient thermal transport calculation by setting the bottom surface of the structure to a constant temperature (300 K), enforcing temperatures to be continuous across interfaces, and treating all other boundary surfaces as thermal insulating (theoretical estimates suggest that the heat losses from radiation and convection are negligible; see Supporting Information, part 8) yields the timedependent temperature distribution. Calculation of thermal expansion uses a fully constrained bottom surface, traction-free boundaries for all other surfaces and no-slip condition at all interfaces. Table S1 lists the parameters used in the simulation. To ensure steady-state behavior, the results (i.e., the amplitude of the out-of-plane thermal expansion at the top surface) are extracted from the final cycle of a simulation that includes 30-50 cycles of time oscillation (higher frequencies used more cycles). At small thicknesses of the PMMA, the interfacial thermal resistance at the PMMA/SiO<sub>2</sub> boundary is no longer negligible.<sup>49,50</sup> Here, the temperature continuity assumption leads to results that underestimate the actual temperature rise in the PMMA and thereby the thermal expansion. As an example, the expansion is underestimated by  ${\sim}12\%$  for the case of PMMA (25 nm)/SiO<sub>2</sub> (90 nm) and  $\sim$ 6% for the case of PMMA (120 nm)/SiO<sub>2</sub> (200 nm) if the PMMA/SiO<sub>2</sub> interfacial thermal resistance<sup>51</sup> is  $\sim 10^{-8}$  m<sup>2</sup> kW<sup>-1</sup>. The effect on the fwhm of the profiles is smaller than 2% in both cases.

For the 3D model used to simulate the case of a SWNT with a point defect, the power output per unit length  $Q(x) = [Q_{def}\delta(x - x_0) + Q_{ave}(x)](1 + \cos(2\omega t))$  is used. Here,  $x_0 = 2.05 \ \mu$ m is the position of the defect,  $Q_{def}$  and  $Q_{ave}$  is the power density of the defect and the rest of the SWNT, respectively. The overall dimensions of the simulation are  $3.4 \ \mu$ m (length)  $\times 3 \ \mu$ m (width), with PMMA (25 nm)/SiO<sub>2</sub> (90 nm) and Si ignored for simplicity. Frequency is set as 30 kHz. All boundary conditions are set to be the same as those in the 2D case (*i.e.*, fixed temperature and motion at the bottom surface; thermal insulation and free of motion for all other surfaces), except for the additional interfacial thermal resistance of  $1 \times 10^{-8} \ m^2 \ kW^{-1}$  at the PMMA/SiO<sub>2</sub> interface and  $1/g_{int}$  for the interface between SWNT and surrounding materials (PMMA/SiO<sub>2</sub>). Five time cycles of oscillation are simulated, and results are extracted from the final cycle.

*Conflict of Interest:* The authors declare no competing financial interest.

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Supporting Information Available: (1) Details of the SJEM setup, (2) details of the 2D analytical model for thermal transport and thermo-mechanical response, (3) properties of the

materials used in the analytical model and FEA, (4) additional 2D FEA, (5) validation of the 2D models, (6) tip—sample interaction and the resonant enhancement effect, (7) simulation of thermomechanical response for point defects in SWNTs, (8) justification of the adiabatic thermal transport boundary condition used at the top surface of PMMA. This material is available free of charge *via* the Internet at http://pubs.acs.org.

### **REFERENCES AND NOTES**

- 1. Avouris, P.; Chen, Z. H.; Perebeinos, V. Carbon-Based Electronics. *Nat. Nanotechnol.* **2007**, *2*, 605–615.
- Franklin, A. D.; Luisier, M.; Han, S. J.; Tulevski, G.; Breslin, C. M.; Gignac, L.; Lundstrom, M. S.; Haensch, W. Sub-10 nm Carbon Nanotube Transistor. *Nano Lett.* **2012**, *12*, 758–762.
- 3. Naeemi, A.; Meindl, J. D. Carbon Nanotube Interconnects. Annu. Rev. Mater. Res. 2009, 39, 255–275.
- Freitag, M.; Johnson, A. T.; Kalinin, S. V.; Bonnell, D. A. Role of Single Defects in Electronic Transport through Carbon Nanotube Field-Effect Transistors. *Phys. Rev. Lett.* **2002**, *89*, 216801.
- Purewal, M. S.; Hong, B. H.; Ravi, A.; Chandra, B.; Hone, J.; Kim, P. Scaling of Resistance and Electron Mean Free Path of Single-Walled Carbon Nanotubes. *Phys. Rev. Lett.* 2007, *98*, 186808.
- Pop, E.; Mann, D. A.; Goodson, K. E.; Dai, H. J. Electrical and Thermal Transport in Metallic Single-Wall Carbon Nanotubes on Insulating Substrates. J. Appl. Phys. 2007, 101, 093710.
- Xiong, F.; Liao, A.; Pop, E. Inducing Chalcogenide Phase Change with Ultra-Narrow Carbon Nanotube Heaters. *Appl. Phys. Lett.* **2009**, *95*, 243103.
- Deshpande, V. V.; Hsieh, S.; Bushmaker, A. W.; Bockrath, M.; Cronin, S. B. Spatially Resolved Temperature Measurements of Electrically Heated Carbon Nanotubes. *Phys. Rev. Lett.* 2009, *102*, 105501.
- Li, Q. W.; Liu, C. H.; Wang, X. S.; Fan, S. S. Measuring the Thermal Conductivity of Individual Carbon Nanotubes by the Raman Shift Method. *Nanotechnology* 2009, 20, 145702.
- Tsai, C. L.; Liao, A.; Pop, E.; Shim, M. Electrical Power Dissipation in Semiconducting Carbon Nanotubes on Single Crystal Quartz and Amorphous SiO<sub>2</sub>. *Appl. Phys. Lett.* **2011**, *99*, 053120.
- 11. Estrada, D.; Pop, E. Imaging Dissipation and Hot Spots in Carbon Nanotube Network Transistors. *Appl. Phys. Lett.* **2011**, *98*, 073102.
- Hu, X. J.; Panzer, M. A.; Goodson, K. E. Infrared Microscopy Thermal Characterization of Opposing Carbon Nanotube Arrays. J. Heat Transfer 2007, 129, 91–93.
- Shi, L.; Plyasunov, S.; Bachtold, A.; McEuen, P. L.; Majumdar, A. Scanning Thermal Microscopy of Carbon Nanotubes Using Batch-Fabricated Probes. *Appl. Phys. Lett.* 2000, 77, 4295–4297.
- Small, J. P.; Shi, L.; Kim, P. Mesoscopic Thermal and Thermoelectric Measurements of Individual Carbon Nanotubes. *Solid State Commun.* 2003, 127, 181–186.
- Shi, L.; Zhou, J. H.; Kim, P.; Bachtold, A.; Majumdar, A.; McEuen, P. L. Thermal Probing of Energy Dissipation in Current-Carrying Carbon Nanotubes. J. Appl. Phys. 2009, 105, 104306.
- Jo, I.; Hsu, I. K.; Lee, Y. J.; Sadeghi, M. M.; Kim, S.; Cronin, S.; Tutuc, E.; Banerjee, S. K.; Yao, Z.; Shi, L. Low-Frequency Acoustic Phonon Temperature Distribution in Electrically Biased Graphene. *Nano Lett.* **2011**, *11*, 85–90.
- Yu, Y. J.; Han, M. Y.; Berciaud, S.; Georgescu, A. B.; Heinz, T. F.; Brus, L. E.; Kim, K. S.; Kim, P. High-Resolution Spatial Mapping of the Temperature Distribution of a Joule Self-Heated Graphene Nanoribbon. *Appl. Phys. Lett.* **2011**, *99*, 183105.
- Shi, L.; Majumdar, A. Thermal Transport Mechanisms at Nanoscale Point Contacts. J. Heat Transfer 2002, 124, 329–337.
- McConney, M. E.; Kulkarni, D. D.; Jiang, H.; Bunning, T. J.; Tsukruk, V. V. A New Twist on Scanning Thermal Microscopy. *Nano Lett.* **2012**, *12*, 1218–1223.



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- Menges, F.; Riel, H.; Stemmer, A.; Gotsmann, B. Quantitative Thermometry of Nanoscale Hot Spots. *Nano Lett.* 2012, *12*, 596–601.
- Shi, L.; Kwon, O.; Miner, A. C.; Majumdar, A. Design and Batch Fabrication of Probes for Sub-100 nm Scanning Thermal Microscopy. J. Microelectromech. Syst. 2001, 10, 370–378.
- Kim, K.; Jeong, W. H.; Lee, W. C.; Reddy, P. Ultra-High Vacuum Scanning Thermal Microscopy for Nanometer Resolution Quantitative Thermometry. ACS Nano 2012, 6, 4248–4257.
- 23. Kim, K.; Chung, J.; Hwang, G.; Kwon, O.; Lee, J. S. Quantitative Measurement with Scanning Thermal Microscope by Preventing the Distortion Due to the Heat Transfer through the Air. *ACS Nano* **2011**, *5*, 8700–8709.
- Park, K.; Cross, G. L. W.; Zhang, Z. M. M.; King, W. P. Experimental Investigation on the Heat Transfer between a Heated Microcantilever and a Substrate. *J. Heat Transfer* 2008, *130*, 102401.
- Majumdar, A.; Varesi, J. Nanoscale Temperature Distributions Measured by Scanning Joule Expansion Microscopy. *J Heat Transfer* **1998**, *120*, 297–305.
- Varesi, J.; Majumdar, A. Scanning Joule Expansion Microscopy at Nanometer Scales. *Appl. Phys. Lett.* **1998**, *72*, 37–39.
- Ho, X. N.; Ye, L. N.; Rotkin, S. V.; Xie, X.; Du, F.; Dunham, S.; Zaumseil, J.; Rogers, J. A. Theoretical and Experimental Studies of Schottky Diodes That Use Aligned Arrays of Single-Walled Carbon Nanotubes. *Nano. Res.* **2010**, *3*, 444–451.
- Gurrum, S. P.; King, W. P.; Joshi, Y. K.; Ramakrishna, K. Size Effect on the Thermal Conductivity of Thin Metallic Films Investigated by Scanning Joule Expansion Microscopy. *J Heat Transfer* **2008**, *130*, 082403.
- Bolte, J.; Niebisch, F.; Pelzl, J.; Stelmaszyk, P.; Wieck, A. D. Study of the Hot Spot of an In-Plane Gate Transistor by Scanning Joule Expansion Microscopy. J. Appl. Phys. 1998, 84, 6917–6922.
- Grosse, K. L.; Bae, M. H.; Lian, F. F.; Pop, E.; King, W. P. Nanoscale Joule Heating, Peltier Cooling and Current Crowding at Graphene—Metal Contacts. *Nat. Nanotechnol.* 2011, *6*, 287–290.
- Liao, A.; Alizadegan, R.; Ong, Z. Y.; Dutta, S.; Xiong, F.; Hsia, K. J.; Pop, E. Thermal Dissipation and Variability in Electrical Breakdown of Carbon Nanotube Devices. *Phys. Rev. B* 2010, *82*, 205406.
- Jin, S. H.; Islam, A. E.; Kim, T. I.; Kim, J. H.; Alam, M. A.; Rogers, J. A. Sources of Hysteresis in Carbon Nanotube Field-Effect Transistors and Their Elimination *via* Methylsiloxane Encapsulants and Optimized Growth Procedures. *Adv. Funct. Mater.* 2012, *22*, 2276–2284.
- Joh, D. Y.; Herman, L. H.; Ju, S. Y.; Kinder, J.; Segal, M. A.; Johnson, J. N.; Chan, G. K. L.; Park, J. On-Chip Rayleigh Imaging and Spectroscopy of Carbon Nanotubes. *Nano Lett.* 2011, *11*, 1–7.
- Liu, W. J.; Etessam-Yazdani, K.; Hussin, R.; Asheghi, M. Modeling and Data for Thermal Conductivity of Ultrathin Single-Crystal Soi Layers at High Temperature. *IEEE Trans. Electron Devices* 2006, *53*, 1868–1876.
- Ju, Y. S.; Goodson, K. E. Process-Dependent Thermal Transport Properties of Silicon-Dioxide Films Deposited Using Low-Pressure Chemical Vapor Deposition. J. Appl. Phys. 1999, 85, 7130–7134.
- Assael, M. J.; Botsios, S.; Gialou, K.; Metaxa, I. N. Thermal Conductivity of Polymethyl Methacrylate (PMMA) and Borosilicate Crown Glass Bk7. *Int. J. Thermophys.* 2005, 26, 1595–1605.
- Tsutsumi, N.; Kiyotsukuri, T. Measurement of Thermal-Diffusivity for Polymer Film by Flash Radiometry. *Appl. Phys. Lett.* **1988**, *52*, 442–444.
- Okada, Y.; Tokumaru, Y. Precise Determination of Lattice-Parameter and Thermal-Expansion Coefficient of Silicon between 300-K and 1500-K. J. Appl. Phys. **1984**, 56, 314– 320.
- Blech, I.; Cohen, U. Effects of Humidity on Stress in Thin Silicon Dioxide Films. J. Appl. Phys. 1982, 53, 4202–4207.

- 40. Chou, S. Y.; Krauss, P. R. Imprint Lithography with Sub-10 nm Feature Size and High Throughput. *Microelectron. Eng.* **1997**, *35*, 237–240.
- Wortman, J. J.; Evans, R. A. Youngs Modulus Shear Modulus and Poissons Ratio in Silicon and Germanium. J. Appl. Phys. 1965, 36, 153–156.
- 42. Kim, M. T. Influence of Substrates on the Elastic Reaction of Films for the Microindentation Tests. *Thin Solid Films* **1996**, 283, 12–16.
- Wu, W. L.; Vanzanten, J. H.; Orts, W. J. Film Thickness Dependent Thermal-Expansion in Ultrathin Poly(methyl methacrylate) Films on Silicon. *Macromolecules* **1995**, *28*, 771–774.
- Fan, Y. W.; Goldsmith, B. R.; Collins, P. G. Identifying and Counting Point Defects in Carbon Nanotubes. *Nat. Mater.* 2005, 4, 906–911.
- Xiao, J. L.; Dunham, S.; Liu, P.; Zhang, Y. W.; Kocabas, C.; Moh, L.; Huang, Y. G.; Hwang, K. C.; Lu, C.; Huang, W.; Rogers, J. A. Alignment Controlled Growth of Single-Walled Carbon Nanotubes on Quartz Substrates. *Nano Lett.* **2009**, *9*, 4311–4319.
- Hong, S. W.; Banks, T.; Rogers, J. A. Improved Density in Aligned Arrays of Single-Walled Carbon Nanotubes by Sequential Chemical Vapor Deposition on Quartz. *Adv. Mater.* 2010, *22*, 30–30.
- Kang, S. J.; Kocabas, C.; Kim, H. S.; Cao, Q.; Meitl, M. A.; Khang, D. Y.; Rogers, J. A. Printed Multilayer Superstructures of Aligned Single-Walled Carbon Nanotubes for Electronic Applications. *Nano Lett.* **2007**, *7*, 3343–3348.
- Kocabas, C.; Pimparkar, N.; Yesilyurt, O.; Kang, S. J.; Alam, M. A.; Rogers, J. A. Experimental and Theoretical Studies of Transport through Large Scale, Partially Aligned Arrays of Single-Walled Carbon Nanotubes in Thin Film Type Transistors. *Nano Lett.* **2007**, *7*, 1195–1202.
- Cahill, D. G. Heat Transport in Dielectric Thin Films and at Solid–Solid Interfaces. *Microscale Therm. Eng.* 1997, 1, 85– 109.
- Lee, S. M.; Cahill, D. G. Heat Transport in Thin Dielectric Films. J. Appl. Phys. 1997, 81, 2590–2595.
- Losego, M. D.; Moh, L.; Arpin, K. A.; Cahill, D. G.; Braun, P. V. Interfacial Thermal Conductance in Spun-Cast Polymer Films and Polymer Brushes. *Appl. Phys. Lett.* **2010**, *97*, 011908.



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