

back to its initial shape, it applies compressive stresses to the circuit sheet. Those stresses spontaneously lead to a complex pattern of buckling, to create a geometry that then allows the circuit to be folded, or stretched, in different directions to conform to a variety of complex shapes or to accommodate mechanical deformations during use.

The researchers constructed integrated circuits consisting of transistors, oscillators, logic gates and amplifiers. The circuits exhibited extreme levels of bendability and stretchability, with electronic properties comparable to those of similar circuits built on conventional silicon wafers.

The new design and construction strategies represent general and scalable routes to high-performance, foldable and stretchable electronic devices that can incorporate established, inorganic electronic materials whose fragile, brittle mechanical properties would otherwise preclude their use, the researchers report.

'We're opening an engineering design space for electronics and optoelectronics that goes well beyond what planar configurations on semiconductor wafers can offer,' Rogers said.

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Source: University of Illinois at Urbana-Champaign

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